

Practical Low Power Digital Vlsi Design By Gary K Yeap

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Practical Low Power Digital VLSI Design emphasizes the optimization and trade-off techniques that involve power dissipation, in the hope that the readers are better prepared the next time they are presented with a low power design problem.

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Low-Power Digital VLSI Design: Circuits and Systems addresses both process technologies and device modeling. Power dissipation in CMOS circuits, several practical circuit examples, and low-power techniques are discussed. Low-voltage issues for digital CMOS and BiCMOS circuits are emphasized.

Low-Power Digital VLSI Design: Circuits and Systems ...
Practical Low Power Digital VLSI Design emphasizes the optimization and trade-off techniques that involve power dissipation, in the hope that the readers are better prepared the next time they are presented with a low power design problem. The book highlights the basic principles, methodologies and techniques that are common to most CMOS digital designs.

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• Gray-code counter is more power efficient.code counter is more power efficient. G. K. Yeap, Practical Low Power Digital VLSI Design, Boston: Kluwer Academic Publishers (now Springer) 1998 National Central University EE4012VLSI Design 30 Kluwer Academic Publishers (now Springer), 1998. Source: Prof. V. D. Agrawal

Chapter 4 Low-Power VLSI DesignPower VLSI Design
Summary: Practical Low Power Digital VLSI Design emphasizes the optimization and trade-off techniques that involve power dissipation, in the hope that the readers are better prepared the next time they are. presented with a low power design problem.

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Low-Power VLSI Circuits and Systems Ajit Pal (auth.) The book provides a comprehensive coverage of different aspects of low power circuit synthesis at various levels of design hierarchy; starting from the layout level to the system level.

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In any digital system, memory is main part and no digital systemcan be total without memories. Now a days embedded system andcompact size devices are developed. So low power consumption isan significant issue in system design. In VLSI design, the main issuesare increased packing density, scaling in silicon technology andoptimizing the speed.